



Learn Beyond

KPR Institute of Engineering and Technology

(Autonomous, NAAC "A")

Avinashi Road, Arasur, Coimbatore.

Phone: 0422-2635600**Web:** kpriet.ac.in**Social:** kpriet.ac.in/social**EC001****NBA Accredited**
(CSE, ECE, EEE,
MECH, CIVIL)**ONE CREDIT COURSE "DIGITAL CIRCUIT DESIGN USING CADENCE"**

Event No	EC001
Organizing Department	Electronics and Communication Engineering
Associate Dept. NSC	Electronics and Communication Engineering
Date	03/06/2024 to 04/06/2024 (2 Days)
Time	09:30 AM to 04:00 PM
Event Type	One Credit Course (OCC)
Event Level	Dept. Level
Venue	DSN LAB
Total Participants	31
Students - Internal	31

Related SDG



Resource Persons

Sl	Type	Name	Designation	Company	Email	Phone
1	Resource Person	Navaneethakrishnan	Sr.Application Engineer	Entuple Technologies Pvt Ltd	navaneethakrishnan.v@entuple.com	xxxxxxxxxx

Involved Staffs

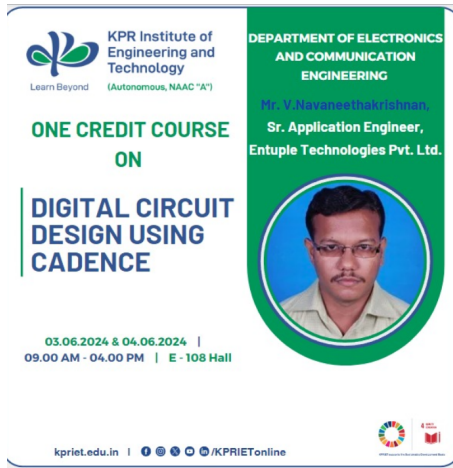
Sl	Name	Role
1	Jai Shankar B	Coordinator
2	Muralidharan J	Coordinator

Outcome

Students are able to design combinational and sequential circuits using Cadence

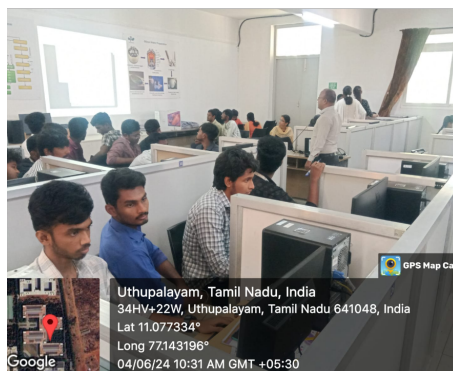
Event Summary

The program is planned for II year students (31 students) of ECE A and B who are having basic knowledge in digital electronics. It was conducted from 03.06.24 to 04.06.24. The resource person started the session with basic concepts of Full and semi custom IC design flow. Then Verilog HDL basic commands are discussed. Simple programs are written using HDL and simulation outputs are verified. The tools in cadence for synthesis are explained through examples. At the end of day 1, students are well versed in RTL analysis and synthesis of combinational circuits. On 04.06.24, the resource person demonstrated the sequential circuits design and Finite State Machine controller design using examples. In the second session, physical implementation using Innovus tool is discussed which includes floor planning, power planning, placement and routing. The students are able to obtain GDSII generation at the end of the program. The coordinators Dr.B.Jaishankar and Dr.J.Muralidharan conducted the assessment of this program through quiz prepared by the resource person. The feedback about the program is collected from the students for further analysis.



The poster features the KPR Institute of Engineering and Technology logo on the left. The main text reads: "ONE CREDIT COURSE ON DIGITAL CIRCUIT DESIGN USING CADENCE". On the right, it identifies the instructor as "Mr. V. Navaneethakrishnan, Sr. Application Engineer, Entuple Technologies Pvt. Ltd." and includes a circular portrait of him. The dates and time are listed as "03.06.2024 & 04.06.2024 | 09.00 AM - 04.00 PM | E - 108 Hall". At the bottom, there is a website link "kpri.edu.in" and social media icons for Facebook, Instagram, and YouTube.

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